

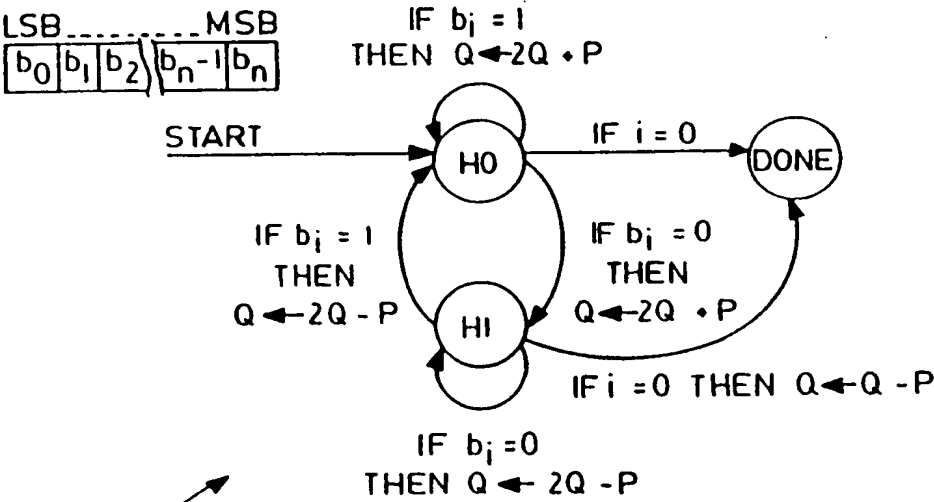
FIG. 1

2025

$$\begin{array}{l}
 629 = \begin{array}{cccccccccccc}
 2^9 & 2^8 & 2^7 & 2^6 & 2^5 & 2^4 & 2^3 & 2^2 & 2^1 & 2^0 \\
 \hline
 1 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 & 1 \\
 \hline
 \diamond 1 & \diamond 1 & -1 & -1 & \diamond 1 & \diamond 1 & \diamond 1 & -1 & \diamond 1 & -1 \\
 \hline
 \end{array} \\
 = \begin{array}{cccccccccccc}
 2^9 & \diamond(2^8 & -2^7 & -2^6) & \diamond 2^5 & \diamond 2^4 & \diamond(2^3 & -2^2) & \diamond(2^1 & -2^0) \\
 \hline
 \end{array}
 \end{array}$$

$$\begin{aligned}
628 &= 2^9 && 2^6 & 2^5 & 2^4 && 2^2 \\
&= && 1 & 0 & 0 & 1 & 1 & 0 & 1 & 0 & 0 \\
&= && \diamond 1 & \diamond 1 & -1 & -1 & \diamond 1 & \diamond 1 & -1 & \diamond 1 & -1 \\
&= && 2^9 & \diamond (2^8 - 2^7 - 2^6) & \diamond 2^5 & \diamond 2^4 & \diamond (2^3 - 2^2) & \diamond (2^1 - 2^0)
\end{aligned}$$

FIG. 2



STATE	INPUT	NEXT	ACTION
H0	i < 0	DONE	SUBTRACT
H0	b _i = 0	H1	DOUBLE, ADD
H0	b _i = 1	H0	DOUBLE, ADD
H1	i < 1	DONE	
H1	b _i = 0	H1	DOUBLE, SUBTRACT
H1	b _i = 1	H0	DOUBLE, SUBTRACT

FIG. 3

BEGIN :

i := N	; START FROM MSB	L 1
Q := 0	; INITIALIZE ACCUMULATOR	L 2
H := 0	; INITIALIZE STATE	L 3

LOOP :

	; FOR ALL BITS	
Q := Q + Q	; DOUBLE ACCUMULATOR	L 4
IF H = 0	; IF H STATE IS SET	L 5
Q := Q + P	; ADD BASE POINT TO ACCUMULATOR	L 6
GOTO ENDLOOP		L 7
ELSE	; ELSE	
Q := Q + (-P)	; SUBTRACT BASE POINT	L 8
GOTO ENDLOOP		L 9

ENDLOOP :

H := b[i]	; SET H STATE TO VALUE OF b[i]	L 10
i := i - 1	; PROCESS NEXT BIT	L 11
IF i > 0	; IF BIT EXISTS	L 12
GOTO LOOP	; CONTINUE AT TOP OF LOOP	L 13
IF H = 0	; IF EXISTING FROM H = 0 STATE	L 14
Q := Q + (-P)	; CORRECT RESULT BY FINAL SUBTRACT	L 15
END		L 16

FIG. 4

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BEGIN :
    i := N      ; START FROM MSB          LL1
    Q := 0      ; INITIALIZE ACCUMULATOR  LL2

H0 :      ; STATE ENTRY POINT
    Q := Q * Q  ; DOUBLE ACCUMULATOR      LL3
    Q := Q * P  ; ADD BASE POINT TO ACCUMULATOR LL4
    GOTO ENDLOOP ; BRANCH TO END OF LOOP TESTS LL5

H1 :      ; STATE ENTRY POINT
    Q := Q * Q  ; DOUBLE ACCUMULATOR      LL6
    Q := Q * (-P) ; SUBTRACT BASE POINT FROM LL7
    ACCUMULATOR
    GOTO ENDLOOP ; BRANCH TO END OF LOOP TESTS LL8

ENDLOOP :      ; END OF LOOP TESTS
    IF b[i]=1   ; IF CURRENT BIT IS SET      LL9
        GOTO NEXT H0 ; FOLLOW H0 PATH          LL10
    ; ELSE FALL INTO H1 PATH

NEXT H1 :      ; H1 PATH
    i := i - 1  ; PROCESS NEXT BIT          LL11
    IF i > 0     ; IF BIT EXISTS              LL12
        GOTO H1  ; EXECUTE H1 STATE          LL13
    Q := Q * (-P) ; ELSE CORRECT RESULT AND END LL14
    END          LL15

NEXT H0 :      ; H0 PATH
    i := i - 1  ; PROCESS NEXT BIT          LL16
    IF i > 0     ; IF BIT EXISTS              LL17
        GOTO H0  ; EXECUTE H0 STATE          LL18
    END          LL19

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FIG. 5

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BEGIN :  
    i := N  
    Q := 1  
  
H0 :  
    Q := Q · Q ( $Q^2$ )  
    Q := Q · M  
    GOTO ENDLOOP  
  
H1 :  
    Q := Q · Q  
    Q := Q / M ( $Q \cdot M^{-1}$ )  
  
ENDLOOP :  
    IF b[i] = 1 GOTO ENDLOOP  
  
NEXT H1 :  
    i = i - 1  
    IF i > 0  
        GOTO H1  
    Q = Q / M  
    END  
  
NEXT H0 :  
    i = i - 1  
    IF i > 0  
        GOTO H0  
    END
```

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FIG. 6

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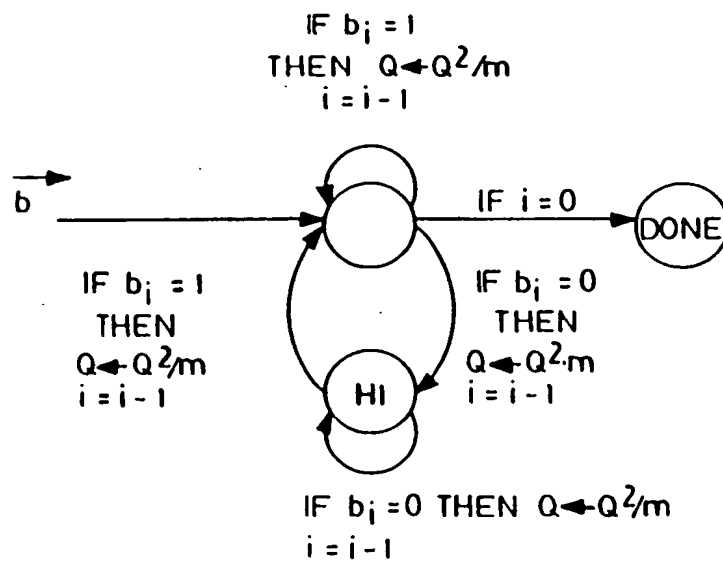


FIG. 7

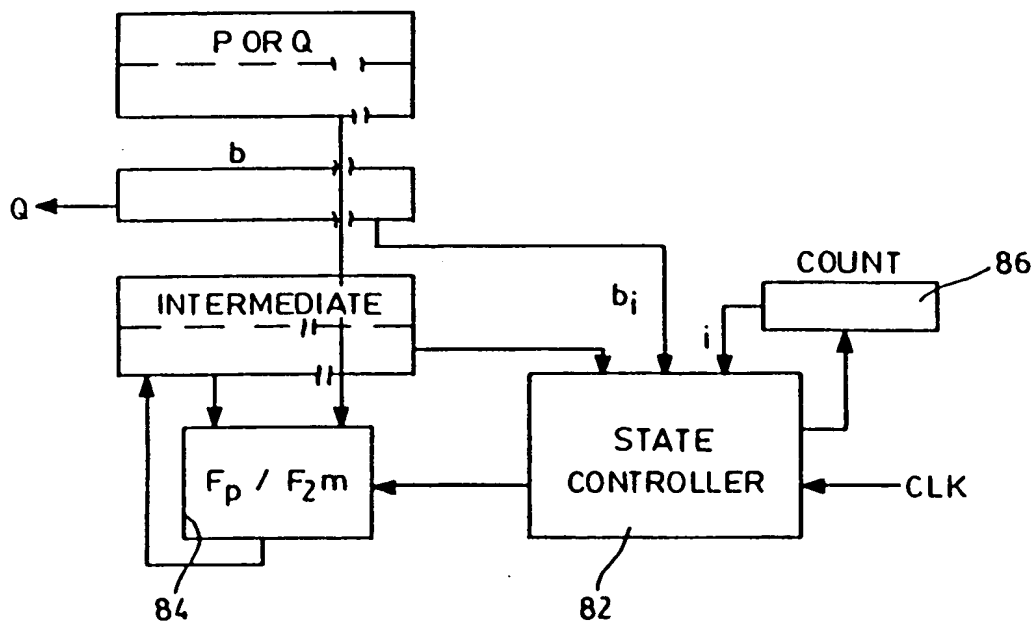


FIG. 8